

L Number	Hits	Search Text	DB	Time stamp
7	2	("5867399").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/06 08:55
8	2	("5623418").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/06 08:58
9	2	("5544067").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/06 08:58
10	2	("5220512").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/06 09:04
11	2	("5222030").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/06 09:05
-	2	("5867399").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 11:01
-	341528	(IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:16
-	105	((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 15:21
-	2	(return near3 path near3 current) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3))) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:46
-	1147	(current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:47
-	1	((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and ((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:48
-	6080	716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:48
-	12	((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:51

-	1	shield\$3 and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:52
-	6	shield\$3 and (((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:59
-	38	(shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:09
-	14	716/\$.ccls. and ((shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:11
-	1	(return near1 path) and (716/\$.ccls. and ((shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:11
-	6	(shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:13
-	1	((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and ((shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/03 08:43
-	73	((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 15:24

-	48	(ASIC or (application adj1 specific adj1 integrated adj1 circuit) or AMS or (analog adj2 mixed adj1 signal)) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 15:26
-	19	parameterized and ((ASIC or (application adj1 specific adj1 integrated adj1 circuit) or AMS or (analog adj2 mixed adj1 signal)) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 15:28
-	2	("6342823").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/03 08:43